

[PROCESS AID DESIGN METHOD, SYSTEM AND PROGRAM PRODUCT]

Abstract

A method, system and program product for generating a process aid on a wafer are disclosed. A "process aid" can be any device provided on a wafer that assists in some process step, but does not ultimately make up part of a usable die. The invention implements libraries of technology design rules, and process aid parameters, and a process aid instruction file library to allow automatic generation of a process aid according to the technology design rules and parameters. As a result, all the inputs required to build a process aid are available up front, which allows the invention to automatically adjust kerf designs to conform to the new technologies. In addition, the invention provides documentation indicating the inputs and details of the process aid produced.